Memory Module Specifications



HX434C19FBK2/32

32GB (16GB 2G x 64-Bit x 2 pcs.)
DDR4-3466 CL19 288-Pin DIMM Kit



DESCRIPTION

HyperX HX434C19FBK2/32 is a kit of two 2G x 64-bit (16GB) DDR4-3466 CL19 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 1G x 8-bit FBGA components per module. Each module supports Intel® Extreme Memory Profiles (Intel® XMP) 2.0. Total capacity is 32GB. Each module has been tested to run at DDR4-3466 at a low latency timing of 19-23-23 at 1.2V. Additional timing parameters are shown in the Plug-N-Play (PnP) Timing Parameters section below. The JEDEC standard electrical and mechanical specifications are as follows:

Note: The PnP feature offers a range of speed and timing options to support the widest variety of processors and chipsets. Your maximum speed will be determined by your BIOS.

JEDEC/XMP TIMING PARAMETERS

JEDEC/PnP: DDR4-3466 CL19-23-23 @1.2V
 XMP Profile #1: DDR4-3466 CL19-23-23 @1.2V
 XMP Profile #2: DDR4-2933 CL17-19-19 @1.2V

SPECIFICATIONS

| CL(IDD) | 19 cycles |
|---|-------------------|
| Row Cycle Time (tRCmin) | 45.75ns(min.) |
| Refresh to Active/Refresh Command Time (tRFCmin) | 350ns(min.) |
| Row Active Time (tRASmin) | 24.234ns(min.) |
| Maximum Operating Power | TBD W* |
| UL Rating | 94 V - 0 |
| Operating Temperature | 0° C to +85° C |
| Storage Temperature | -55° C to +100° C |
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^{*}Power will vary depending on the SDRAM used.

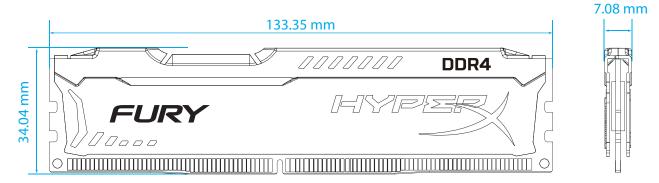
FEATURES

- Power Supply: VDD = 1.2V Typical
- VDDQ = 1.2V Typical
- VPP = 2.5V Typical
- VDDSPD = 2.2V to 3.6V
- On-Die termination (ODT)
- 16 internal banks; 4 groups of 4 banks each
- Bi-Directional Differential Data Strobe
- 8 bit pre-fetch
- Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
- Height 1.340" (34.04mm), w/heatsink

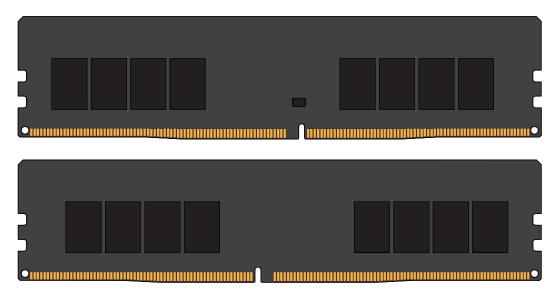
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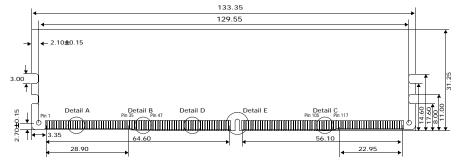
MODULE WITH HEAT SPREADER



MODULE DIMENSIONS



All measurements are in millimeters. (Tolerances on all dimensions are ±0.12 unless otherwise specified)



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